



CHHATTISGARH SWAMI VIVEKANAND TECHNICAL UNIVERSITY, BHILAI
छत्तीसगढ़ स्वामी विवेकानंद तकनीकी विश्वविद्यालय, भिलाई

No : 1590/CSVТУ/Ph.D./2016

Bhilai, Date: 13.6.16

Notice

The following candidates of Electrical Engineering will be interviewed for admission in Ph.D. programme 2015 at CSVТУ conference hall on the dates and time shown below:

Sl. No	Research Centre	Roll.No	Name of the Candidate	Status	Date	Time
1	BIT, Durg	72415023	Arvind Kumar Agrawal	Exempted from written test	22/06/2016	11.00 am
2	BIT, Durg	72415024	Chand Kishor Rahangdale	Exempted from written test	22/06/2016	11.00 am
3	BIT, Durg	72415025	Juli Singh Sadhak	Exempted from written test	22/06/2016	11.00 am
4	BIT, Durg	72415026	Manjusha Silas	Exempted from written test	22/06/2016	11.00 am
5	BIT, Durg	72415027	Ritu Singh	Exempted from written test	22/06/2016	11.00 am
6	BIT, Durg	72415028	Roshni Rahangdale	Exempted from written test	22/06/2016	11.00 am
7	BIT, Durg	72415029	Vineet Dewangan	Exempted from written test	22/06/2016	12.00 noon
8	SSTC, Bhilai	72415030	Naveen Prakash Singh	Exempted from written test	22/06/2016	12.00 noon
9	RCET, Bhilai	72415031	Ashish Sahu	Exempted from written test	22/06/2016	12.00 noon
10	RCET, Bhilai	72415032	Laxmi Devi Sahu	Exempted from written test	22/06/2016	12.00 noon
11	RCET, Bhilai	72415033	Ritesh Chhapre	Exempted from written test	22/06/2016	12.00 noon
12	RCET, Bhilai	72415034	Tanu Rizvis	Exempted from written test	22/06/2016	12.00 noon

Registrar
13.6.16
CSVТУ, BHILAI

Copy to :

1. OSD (Academic)
2. PS to H'VC
3. Research Center
4. Notice Board
5. Master File