# Chhattisgarh Swami Vivekanand Technical University, Bhilai

**Scheme of Teaching and Examination**

**B.E. VII Semester Electronics & Telecommunication Engineering**

<table>
<thead>
<tr>
<th>Sl. No.</th>
<th>Board of Study</th>
<th>Code No.</th>
<th>Subjects</th>
<th>Period Per Week</th>
<th>Scheme of Exam</th>
<th>Total Marks</th>
<th>Credit</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>328731(28)</td>
<td>Microwave Communication and Engineering</td>
<td>4 1 -</td>
<td>ESE CT TA</td>
<td>120 4</td>
<td>5</td>
</tr>
<tr>
<td>1</td>
<td>Electronics &amp; Telecom.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>Electronics &amp; Telecom.</td>
<td>328732(28)</td>
<td>Computer Networks</td>
<td>3 1 -</td>
<td>ESE CT TA</td>
<td>120 4</td>
<td>4</td>
</tr>
<tr>
<td>3</td>
<td>Electronics &amp; Telecom.</td>
<td>328733(28)</td>
<td>Wireless Communications</td>
<td>3 1 -</td>
<td>ESE CT TA</td>
<td>120 4</td>
<td>4</td>
</tr>
<tr>
<td>4</td>
<td>Electronics &amp; Telecom.</td>
<td>328734(28)</td>
<td>Management Concepts &amp; Techniques</td>
<td>3 1 -</td>
<td>ESE CT TA</td>
<td>120 4</td>
<td>4</td>
</tr>
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<td>5</td>
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<td></td>
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<td>3 1 -</td>
<td>ESE CT TA</td>
<td>120 4</td>
<td>4</td>
</tr>
<tr>
<td>6</td>
<td>Electronics &amp; Telecom.</td>
<td>328761(28)</td>
<td>Microwave Communication and Engineering Lab</td>
<td>- - 4</td>
<td>- 20 60</td>
<td>120 4</td>
<td>2</td>
</tr>
<tr>
<td>7</td>
<td>Electronics &amp; Telecom.</td>
<td>328762(28)</td>
<td>Computer Networks Lab</td>
<td>- - 4</td>
<td>- 20 60</td>
<td>120 4</td>
<td>2</td>
</tr>
<tr>
<td>8</td>
<td>Electronics &amp; Telecom.</td>
<td>328763(28)</td>
<td>Advance Communication Lab</td>
<td>- - 4</td>
<td>- 20 60</td>
<td>120 4</td>
<td>2</td>
</tr>
<tr>
<td>9</td>
<td>Electronics &amp; Telecom.</td>
<td>328764(28)</td>
<td>Minor Project</td>
<td>- - 4</td>
<td>100 - 40</td>
<td>140 2</td>
<td>2</td>
</tr>
<tr>
<td>10</td>
<td>Management</td>
<td>328765(28)</td>
<td>Innovative &amp; Entrepreneurial Skills</td>
<td>- - 2</td>
<td>- 40 40</td>
<td>80 1</td>
<td>1</td>
</tr>
<tr>
<td>11</td>
<td>Electronics &amp; Telecom.</td>
<td>328766(28)</td>
<td><strong>Practical Training Evaluation and Library</strong></td>
<td>- - 1</td>
<td>- 40 40</td>
<td>160 31</td>
<td>31</td>
</tr>
</tbody>
</table>

**TOTAL** 16 5 19 620 100 280 1000 31

L-Lecture, T- Tutorial, P - Practical, ESE- End Semester Examination, CT - Class Test, TA - Teacher's Assessment

**To be completed after VI Semester and before the commencement of VII Semester**

**Table - 2**

**Professional Elective - II**

<table>
<thead>
<tr>
<th>Sl. No.</th>
<th>Board of Study</th>
<th>Code No.</th>
<th>Subject</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Electronics &amp; Telecom.</td>
<td>328741(28)</td>
<td>Digital Circuit Design with Verilog HDL</td>
</tr>
<tr>
<td>2</td>
<td>Electronics &amp; Telecom.</td>
<td>328742(28)</td>
<td>System Design with ARM</td>
</tr>
<tr>
<td>3</td>
<td>Electronics &amp; Telecom.</td>
<td>328743(28)</td>
<td>Robotics &amp; Controls</td>
</tr>
<tr>
<td>4</td>
<td>Electronics &amp; Telecom.</td>
<td>328744(28)</td>
<td>Radar and Navigational Aids</td>
</tr>
<tr>
<td>5</td>
<td>Electrical and Electronics</td>
<td>325745(28)</td>
<td>Industrial Automation</td>
</tr>
<tr>
<td>6</td>
<td>Electronics &amp; Telecom.</td>
<td>328746(28)</td>
<td>Neural Network &amp; Fuzzy Logic</td>
</tr>
<tr>
<td>7</td>
<td>Electronics &amp; Telecom.</td>
<td>328747(28)</td>
<td>RF Communication Design</td>
</tr>
<tr>
<td>8</td>
<td>Electronics &amp; Telecom.</td>
<td>328748(28)</td>
<td>VLSI System Design</td>
</tr>
<tr>
<td>9</td>
<td>Electronics &amp; Telecom.</td>
<td>328749(28)</td>
<td>Digital Image Processing</td>
</tr>
</tbody>
</table>

Note (1) - 1/4th of total strength of students subject to minimum of twenty students is required to offer an elective in the college a particular academic session.

Note (2) - Choice of elective course once made for an examination cannot be changed in future examinations.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: **Bachelor of Engineering**
Branch: **Electronics & Telecommunication**
Subject: **Microwave Communication & Engineering**

**Course Objectives:**
1. Learn about wave propagation through waveguide
2. Learn about transmission in rectangular waveguide
3. Learn about microwave tubes
4. Learn about transferred electron device
5. Learn the theory of microwave cavity and there various applications
6. Learn designing of microwave filters

**UNIT – I**  **Introduction to Microwaves and Linear Beam Tubes:** Need of Microwave Communications; Microwave Region and Bands; Advantages and Limitations of Microwave Systems; Failure of Conventional Tubes at High Frequencies; Klystron - Velocity Modulation Power Output and Efficiency; Reflex Klystron - Velocity Modulation Power Output and Efficiency; Helix TWT- Amplification Process and Working.

**UNIT – II**  **Microwave Crossed-Field Tubes:** Difference between Linear Beam Tubes & Crossed Field Tubes, Magnetron- Cavity Magnetron: Principle and Operation, Mode of Oscillation, Strapping and Mode Jumping, Voltage Tunable Magnetron, Inverted Coaxial Magnetron. Forward wave and Backward wave Crossed Field Amplifier.

**UNIT – III**  **Microwave Devices:** Microwave Bipolar Transistor: Structure, Operation, Characteristics and Power Frequency Limitations of Microwave Transistors, Microwave Field-Effect Transistor: JFET, MESFET, MOSFET, Microwave diode: Tunnel Diode, PIN diode, Crystal diode.


**Transfer Electron Devices:** Gunn Diode, Gunn Effect: Principle and Mode of Operation.

**UNIT – V**  **Microwave Components and Measurement:** Rectangular Cavity Resonators, Q of a Cavity Resonator, Reentrant Cavities, Slow-Wave Structure, Microwave Hybrid Circuits, S-parameters and their properties, Wave Guide Tees, Hybrid Ring, Waveguide Corners, Bends and Twists, Two Hole Directional Coupler, S-Matrix, Circulator and Isolator, Hybrid Coupler, Microwave Measurement, Microwave Bench, Precautions, Power Measurement, Bolometric Method, Attenuation, VSWR, Impedance, Frequency and Q of the Cavity.

**Text Books:**
1. Microwave Devices and Circuits by Samuel Y. Liao, 3rd Ed., Pearson Education

**Reference Books:**
2. Microwaves by Gupta, New Age International Publishers
3. Microwave Semiconductor Devices by Roy & Mitra, PHI

**Course outcomes:**
1. Understand the reason why TEM wave are impossible in a Waveguide.
2. Understand the working of Microwave Tubes.
3. Understand the different modes of operation of Gunn Diodes.
4. Understand microwave components such as Tee Junction and Directional Couplers.
5. Understand designing and transformation of Microwave Filters.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: Bachelor of Engineering  
Branch: Electronics & Telecommunication  
Subject: Computer Networks  
Semester: VII  
Code: 328732(28)

Total Theory Periods: 40  
Class Tests: Two (Minimum)  
ESE Duration: Three Hours  
Total Tutorial Periods: 12  
Assignments: Two (Minimum)  
Maximum Marks: 80  
Minimum Marks: 28

Course Objectives:
1. To make students understand the basic model of data communication, OSI Model, TCP/IP suite and various concepts of networking.
2. To make students acquainted with Data Link Layer and various flow control and error control protocol.
3. To familiarize students with different LAN protocols like Ethernet, Token ring and Token Bus and FDDI.
4. To teach students about connecting devices, Network and transport layer protocols.
5. To give knowledge of the Application layer functions, protocols, switching and switched networks like ATM.

UNIT-I  

UNIT-II  

UNIT-III  

UNIT-IV  

UNIT-V  

Text Books:

Reference Books:

Course Outcomes:
1. Students will be able to understand the working of internet based on OSI model and TCP/IP protocol suite.
2. Students will be able to analyze practical requirements of LAN on the basis of various topologies, signalling techniques and various interfaces.
3. Students will have deep understanding of various protocols used at Data Link Layer and will be able to analyze the advantages and disadvantages of various available protocols for flow and error control.
4. Students will be able to analyze various Ethernet standards, other standards and will be able to choose an appropriate standard according to requirement of LAN.
5. Students will be able to identify various internetworking devices and formation of Headers of IP and TCP.
6. Students will get idea about various Application layer functions and some protocols along with switching techniques and ATM.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: Bachelor of Engineering
Branch: Electronics & Telecommunication
Semester: VII
Subject: Wireless Communications
Code: 328733(28)

Total Theory Periods: 40
Class Tests: Two (Minimum)
ESE Duration: Three Hours
Total Tutorial Periods: 12
Assignments: Two (Minimum)
Maximum Marks: 80
Minimum Marks: 28

Course Objectives:
1. To give students brief history of the evolution of mobile communications throughout the world.
2. To give knowledge of cellular concepts and its designing aspects.
3. To give students a detailed overview of GSM, its architecture, interfaces, frames etc.
4. To familiarize students about advanced modulation techniques used in mobile communications.
5. To teach students about the practical limitations on the performance of wireless communication systems.


UNIT –III Global Systems for Mobile: System Architecture, GSM frequency bands, GSM PLMN, GSM subsystems, GSM interface, Mapping of GSM Layer on to OSI Layers, GSM Logical Channel and Frame, Structure, GSM Burst, Data encryption in GSM, Mobility Management.


Textbooks:
1. Wireless Communications by T.S.Rappaport, Pearson Education

Reference books:
1. Mobile Communications – Schiller, Jochen; 2nd Indian Reprint, Pearson Education Asia – Addison Wesley Longman Pte. Ltd.

Course Outcomes:
1. Students will have idea about the growth in mobile communications that gives rise to technological improvements.
2. Students will be able to visualize the use of frequency reuse to increase the systems capacity and also other designing aspects.
3. Students will be able to understand the architecture of the GSM and mechanism to support mobility of the GSM terminals.
4. Students will see how modulation techniques are used to transport the message signal via a radio channel with best possible quality with minimum radio spectrum.
5. Students will be able to understand various transmission problems and their counter measures.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: Bachelor of Engineering
Branch: Electronics & Telecommunication
Subject: Management Concepts and Techniques
Semester: VII
Code: 328734(28)

Total Theory Periods: 40 Total Tutorial Periods: 12
Class Tests: Two (Minimum) Assignments: Two (Minimum)
ESE Duration: Three Hours Maximum Marks: 80

Course Objectives:
1. To enable the students to acquire knowledge about the principles of management
2. To get an idea of human resource management
3. To get an idea about marketing management
4. To give an insight to the production/operation management
5. To give an insight to the organization of a business and public sector companies.


Text Books:
2. Industrial Engineering and Production Management, Martand Telsang, S. Chand
3. Industrial Management and Organization, Ahuja, Khanna Pbs.
4. Industrial Engineering and Management, O. P. Khanna, DRD.

Reference Books:
1. Industrial Organization and Management, Ramchandran, Ramana Mutry, TMH.
2. Management Science, Ramchandra, TMH.
3. Industrial Engineering and Production Management, Mahajan, DRP.

Course Outcome:
At the completion of the course, the student will be able to:
1. Define the concept of management and discuss why organizations are needed, why managers are necessary, and why management is a challenge.
2. Identify the essential characteristics of decision making and indicate the range and types of decisions a manager is asked to make.
3. Analyze the leadership function, recognizing leadership as the relationship between a supervisor and subordinates in an organizational environment.
4. Recognize the symptoms of organizational conflict, describe its sources, and discuss the manager's role in conflict management.
5. Recognize the link between planning and controlling, and the various means by which managers measure and compare performance to objectives.
6. To understand and differentiate between the various types of organizational structures and patterns.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Branch: Electronics & Telecommunication  Semester: VII
Subject: Microwave Communication and Engineering Lab  Code:328761(28)

Total Lab Period: 50  Batch Size: 30
Maximum Marks: 40  Minimum Marks 20

List of Experiments: (At least Ten experiments are to be performed by each student)

1. To measure the VSWR at all the three open ports of a Directional Coupler.
2. To measure the Coupling Factor, directivity and insertion loss of a Directional Coupler.
3. To study mode characteristics of a Reflex Klystron and hence to determine mode number, transit time, Electronic Tuning Sensitivity (ETS) and Electronic Tuning Range (ETR).
4. To study the characteristics of Wave Propagation in a Wave Guide by studying standing wave pattern and hence to plot $\alpha - \beta$ diagram and verify relationship between guide wavelength ($\lambda_g$) and free space wavelength $\lambda$.
5. To study the V-I characteristics of a GUNN Diode and to measure output power and frequency vs voltage.
6. To study and measure square wave modulation through PIN voltage.
7. To energize a GUNN Oscillator.
8. To energize a Reflex Oscillator.
9. To calibrate Phase Shifter.
10. To measure Dielectric Constant.
11. To study the characteristic and behavior of a Magic Tee.
12. To study the characteristics and behavior of Isolator and Circulators.
13. To study the characteristics and behavior of Attenuator (fixed and variable type).
14. To measure Microwave Frequency using Frequency Meter.
16. To study the function of Multiple Directional Coupler by measuring the following parameters:
   a. To measure main-line and auxiliary-line VSWR.
   b. To measure the Coupling Factor and Directivity of the Coupler.
17. To study the characteristics of the Reflex Klystron Tube and to determine its electronic tuning range.
18. To determine the frequency and wave length in a Rectangular Wave Guide working on TE10 mode.
19. To determine the standing wave ratio and reflection coefficient.

List of Equipments Required:

Microwave source, Isolator, Variable attenuator, Fixed Attenuator, Frequency meter, Slotted line, Tunable probe, Circulators, Matched terminations, Gunn/Klystron power supply, Detector mount, Cooling fan, Magic Tee, Phase shifter, Movable short, Dielectric Material.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Branch: Electronics & Telecommunication  Semester: VII
Subject: Computer Networks Lab  Code: 328762(28)

| Total Lab Period: | 50 |
| Minimum Marks  | 40 |
| Batch Size:  | 30 |

List of Experiments: (At least Ten experiments are to be performed by each student)
1. Introduction to Local Area Network with its cables, connectors and topologies.
2. To build CAT-5 UTP Ethernet cable (Cross-over and Straight –Through)
3. Installation of Switches, their cascading and network mapping.
4. Installation of UTP, NIC and LAN card to connect two PC.
5. Case Study of Ethernet (10 base 5, 10 base 2, 10 base T)
7. Installation and working of Telnet (Terminal Network).
8. Installation and working with FTP (File Transfer Protocol).
9. Installation and connecting Computers to share disk and printer
10. Installation of Modem and Proxy Server.
11. Working with Null Modem.
12. Introduction to Server administration.

Recommended Books:-
1. Computer Network and Internet by Douglas E. Comer (Pearson Education)

List of Softwares required:-

List of Hardwares required:-
1. LAN Trainer Kit, LAN Card Cable, Connectors, Switch, cables, Crimping Tools.
List of Experiments: (At least Ten experiments are to be performed by each student)

1. Implementation/simulation of PN sequence generator using linear feedback shift register technique.
2. Implementation/simulation of QAM modulation and demodulation.
3. Implementation/simulation of GMSK modulation and demodulation using GMSK trainer module.
4. Implementation/simulation of MSK modulation and demodulation.
5. Implementation/simulation of MPSK modulation and demodulation.
7. Implementation/simulation of Frequency hopping spread spectrum technique (FHSS).
8. To understand RF environment, GSM technology its network GSM capability and data services using GSM trainer module.
10. Simulation of Huffman code algorithm.
11. Simulation of Run length code algorithm.
13. Simulation of Linear block code algorithm.
15. Simulation of Cyclic code algorithm.
16. Simulation of BHC code algorithm.

List of Equipments Required:

1. PCs, simulation software, trainer kits.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Branch: Electronics & Telecommunication  Semester: VII
Subject: Minor Project  Code: 328764(28)

Total Practical Period: 50

Total Marks in End Semester Examination: 100

- The students are expected to take up a Project under the guidance of a faculty from the Institute.
- The topic of the project should be justified for the degree of BE (Electronics & Telecommunication).
- The project selected should ensure the satisfaction of the urgent need to establish a direct link between education, national development and productivities.
- The students may be asked to work individually or in a group having not more than FOUR students.
- The student/group of student shall collect all necessary information from literature on selected topic/project.
- It should include the scope of project, identification of necessary data, source of data, development of design method and identification, methodology, software analysis.
- Students should deliver a seminar on the selected Project/topic.
- The students are expected to submit the report in standard format approved by the University in partial fulfillment of the requirement for the degree of B.E. (Electronics & Telecommunication).
- There will be an external viva-voce at the end of the semester and the students are to demonstrate the project at the time of viva-voce.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: Bachelor of Engineering
Branch: Electronics & Telecommunication Semester: VII
Subject: Innovative & Entrepreneurial Skill Code: 328765(28)

Total Practical Periods: 28 Total Tutorial Periods: NIL

Unit I  Innovation: Innovation: an abstract concept; creativity, innovation and imagination; types of innovation - classified according to products, processes or business organizations.

Unit II  Entrepreneurship: who is an entrepreneur? Entrepreneurship: A state of Mind, Emergence of entrepreneur; Role of Entrepreneur; A Doer not a Dreamer- Characteristics of an entrepreneur; Factors affecting entrepreneurial growth – Social, cultural, personality factors, psychological and Social Factors. Impact of Entrepreneurship for sustainable development.

Unit III  Difference between entrepreneur and entrepreneurship: Difference between entrepreneur and intrapreneur, Common Entrepreneurial competencies/Traits; Entrepreneurship stimulants, Obstacles inhibiting Entrepreneurship; Types of entrepreneurs, Functions of an entrepreneur.

Unit IV  Identification of Business Opportunities: Introduction, Sources of Business of Product Ideas, Steps in Identification of Business opportunity and its SWOT Analysis.


Reference Books:
1. Competing through Innovation-Bellow & Whittington, Prentice Hall of India
3. Entrepreneurship- Rober D Hisrich, Peters, Shepherd- TMH
4. Entrepreneurship in Action- Coulter, Prentice Hall of India
5. Entrepreneurship Management and Development – Ajith Kumar, HPH
6. Fundamentals of entrepreneurship- Mohanty, PHI

Course Outcomes
Chhattisgarh Swami Vivekananda Technical University, Bhilai

Name of program: Bachelor of Engineering
Branch: Electronics & Telecommunication Semester: VII
Subject: Industrial Automation Code: 325745(25)

Total Theory Periods: 40 Total Tutorial Periods: 12
Class Tests: Two (Minimum) Assignments: Two (Minimum)
ESE Duration: Three Hours Maximum Marks: 80 Minimum Marks: 28

Course Objectives:
1. To develop and apply Mathematical and Engineering skills to identify, formulate and solve industrial process problems.
2. This subject seeks to close the gap between Instrumentation and Mechanical Engineering.
3. This subject provides the knowledge of different types of controller & their applications.
4. This subject provides the basic knowledge of PLC and DCS.


UNIT – IV Programmable Logic Controller: PLC Architecture, Basic Structure, PLC Programming: Ladder Diagram, Ladder Diagram symbols, Ladder Diagram circuits, PLC Communications and Networking, PLC Selection, I/O Quantity and Type, I/O Remoting requirements, Memory size and type, Programmer Units, PLC Installation, Advantages of using PLCs.

UNIT – V Distributed Control System: Introduction, Overview of Distributed Control Systems, DCS Software configuration, DCS Communication, DCS Supervisory Computer Tasks, DCS Integration with PLCs and Computers, Features of DCS, Advantages of DCS.

Text Books:
1. Process Control Instrumentation Technology by C.D. Johnson, PHI

Reference Books:
1. Introduction to Instrumentation & Control by A.K. Ghosh, Eastern Economy Edition
2. Intelligent Instrumentation, by George C. Barney, Prentice Hall India.

Course Outcomes:
The students will be able to:
2. Know the importance of on-off, proportional, integral and derivative modes, composite control modes - PI, PD and PID controllers.
3. Understand, Communication in DCS, DCS system integration with PLC and computers, Data loggers, Data Acquisition systems (DAS), computer control hierarchy levels and Direct Digital control (DDC).
Course Objectives:
1. To understand the fundamentals of ARM processors and architecture.
2. To gain knowledge of internal features of ARM Processors.
3. To understand various standards, protocols and transmission techniques.
4. To have an insight into design methodology.
5. To gain knowledge of design optimization.

UNIT - I  
**Introduction to 16 / 32-bit Microcontrollers:** Introduction to RISC processor, Difference between CISC & RISC, ARM Microcontrollers and Processor Cores, ARM-7/ARM-9 Architecture and Organization, ARM7TDMI Processor Block Diagram, ARM7TDMI Main Processor logic, ARM/THUMB Programming Model, ARM/THUMB Instruction Set, ARM Exception Handling, More ARM Instructions, ARM/THUMB Assembly Programming.

UNIT -II  
**Timers & Interrupts:** Data handling, Interfacing with Memory, Interrupts, Timers, ARM Bus, I/O Devices, Controllers, Simple & Autonomous I/O Controllers,

UNIT -III  

UNIT -IV  
**Design Methodology:** Design Methodology, Design Flow, Architecture Exploration, Functional Design, Functional Verification, Synthesis, Physical Design,

UNIT -V  
**Design Optimization:** Design Optimization, Area Optimization, Timing Optimization, Power Optimization, Design for Test, Fault Models and Fault Simulation, Scan Design and Boundary Scan, Built-In Self Test (BIST), Non-technical Issues.

Text Books

Reference Books:

Course Outcome:
1. Student will be able to understand the fundamental of ARM architecture that includes register array, cache, virtual memory, pipeline and memory management units
2. Student will be able to understand the assembly language instructions for ARM processor
3. Student will be able to understand the internal features of ARM Processor
4. Student is able to create small embedded system using ARM controller and realization of hardware based design through team project
5. Implementation of control system algorithms using ARM Controller
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: **Bachelor of Engineering**

Branch: **Electronics & Telecommunication**

Subject: **Robotics & Controls**

Semester: VII

Code: 328743(28)

<table>
<thead>
<tr>
<th>Total Theory Periods</th>
<th>Total Tutorial Periods</th>
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</thead>
<tbody>
<tr>
<td>40</td>
<td>12</td>
</tr>
</tbody>
</table>

Class Tests: Two (Minimum)

ESE Duration: Three Hours

Maximum Marks: 80

Minimum Marks: 28

**Course Objectives:**

1. To study overview of robot mechanisms, dynamics, and intelligent controls.
2. To study basic robot co-ordinate configurations.

**UNIT – I**


**UNIT – II**

**Coordinate Frames, Mapping and Transforms:** Coordinate Frames, Description of Objects in Space, Transformation of Vectors, Inverting a Homogeneous Transform, Fundamental Rotation Matrices

**UNIT – III**


**UNIT – IV**

**Control of Manipulators:** Open- and Close-Loop Control, The Manipulator Control Problem, Linear Control Schemes, Characteristics of Second-Order Linear Systems, Linear Second-Order SISO Model of a Manipulator Joint, Joint Actuators, Partitioned PD Control Scheme, PID Control Scheme, Computed Torque Control, Force Control of Robotic Manipulators, Description of Force-Control Tasks, Force-Control Strategies, Hybrid Position/Force Control, Impedance Force/Torque Control.

**UNIT – V**


**Text Books**


**Reference Books**

2. Kinematics and Synthesis of Linkages by Hartenberg and Denavit, McGraw Hill.

**Course Outcomes:**

By studying this course, students will be

1. Familiar with the concept development and key components of robotics technologies.
2. Understand basic mathematic manipulations of spatial coordinate representation and transformation.
3. Able to undertake practical robotics experiments that demonstrate the above skills.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: Bachelor of Engineering  
Branch: Electronics & Telecommunication  
Subject: Radar & Navigational Aids  
Semester: VII  
Code: 328744(28)

Total Theory Periods: 40  
Class Tests: Two (Minimum)  
ESE Duration: Three Hours  
Total Tutorial Periods: 12  
Assignments: Two (Minimum)  
Maximum Marks: 80  
Minimum Marks: 28

Course Objectives:
1. Main objective of this course is to make the students understand the basic concept in the field of Radar and Navigational aids.
2. Students are taught about different types of Radar Systems.


UNIT – II  MTI And Pulse Doppler Radar: Introduction to Doppler and MTI Radar, Delay Line Cancellers, Staggered PRF, Range Gated Doppler Filter, Limitations to MTI Performance, Tracking with Radar, Monopulse Tracking, Conical Scan and Sequential Lobing, Limitations to Tracking Accuracy, Low Angle Tracking, Tracking in range, Comparison of Trackers.


UNIT – V  Radar Transmitter and Receiver: Radar Receiver, Receiver Noise Figure, Superheterodyne Receiver, Duplexers and Receiver Protectors, Radar Displays, introduction to ECM and ECCM, Linear Beam Power Tubes, Solid State Power Sources, Magnetron.

Text Books:

Reference Books:

Course Outcomes:
1. To become familiar with fundamentals of Radar.
2. To gain in depth knowledge about the different types of Radar and their operation.
3. Need for signal detection in Radar and various Radar signal detection techniques.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: Bachelor of Engineering  
Branch: Electronics & Telecommunication  
Subject: Digital Circuit Design with Verilog HDL  
Semester: VII  
Code: 328741(28)

Total Theory Periods: 40  
Total Tutorial Periods: 12  
Class Tests: Two (Minimum)  
Assignments: Two (Minimum)  
ESE Duration: Three Hours  
Maximum Marks: 80  
Minimum Marks: 28

Course Objectives:
1. To understand basics of Verilog HDL Language, including its use in synthesis of digital designs.
2. To gain knowledge of modeling, simulation and verification of designs with Verilog HDL.
3. To understand combinational circuit design of digital systems with Verilog HDL.
4. To understand sequential circuit design of digital systems with Verilog HDL.
5. To understand designing using Mealy State and Moore State Model.

UNIT-I Overview of Digital Design with Verilog-HDL: Emergence of HDLs, Typical Design Flow, Importance of HDLs, Popularity of Verilog HDLs. Design Methodologies, Modules, Instances, Lexical conventions, Data Types, System Tasks and Compiler directives.


UNIT -V FSM: Basic Design Steps, State Diagram, State Table, State Assignment, State Assignment Problem, One Hot Encoding, Mealy State Model, Moore State Model, Design Example: Serial Adder, Vending Machine, Bus Architecture.

Text Books:

Reference Books:
2. Verilog Hdl Synthesis: A Practical Primer, J. Bhasker PHI.

Course Outcome: Students will be able to:
1. Use VLSI design methodologies to understand and design complex digital systems.
2. Create circuits that realize specified digital functions.
3. Identify logic and technology-specific parameters to control the functionality, timing, power, and parasitic effects.
4. Complete a significant VLSI design project having a set of objective criteria & design constraints.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: Bachelor of Engineering  
Branch: Electronics & Telecommunication  
Subject: Neural Network and Fuzzy Logic  
Semester: VII  
Code: 328746(28)

Total Theory Periods: 40  
Class Tests: Two (Minimum)  
ESE Duration: Three Hours  
Total Tutorial Periods: 12  
Assignments: Two (Minimum)  
Maximum Marks: 80  
Minimum Marks: 28

Course Objectives:
1. The main objective of this course is to provide the student with the basic understanding of neural networks and fuzzy logic fundamentals, Program the related algorithms and Design the required and related systems.
2. To learn the various architectures of building an ANN and its applications.
3. To learn the advanced methods of representing information in ANN like self organizing networks, associative and competitive learning.
4. To learn the fundamentals of Crisp sets, Fuzzy sets and Fuzzy Relations.

UNIT-I  
Introduction to Artificial Neural Networks: Elementary Neurophysiology, Models of a Neuron, Neural Networks viewed as Directed Graphs, Feedback from Neurons to ANN, Artificial Intelligence and Neural Networks, Network Architectures, Single-Layered Feed forward Networks, Multi-Layered Feed Forward Networks, Recurrent Networks, Topologies.

UNIT-II  
Learning and Training: Activation and Synaptic Dynamics, Hebbian, Memory based, Competitive, Error-Correction Learning, Credit Assignment Problem: Supervised and Unsupervised learning, Memory models, Stability and Convergence, Recall and Adaptation.

UNIT-III  

UNIT-IV  
Applications: Talking Network and Phonetic Typewriter, Speech Generation and Speech Recognition, Neocognitron, Character Recognition and Handwritten Digit Recognition, Pattern Recognition Applications.

UNIT-V  

Text Books:
1. Artificial Neural Networks by B. Yagna Narayan, PHI.

Reference Books:
1. Neural Networks by James A. Freeman and David M. Strapeluns, Prentice Hall.
2. Neural Network & Fuzzy System by Bart Kosko, PHI.

Course Outcomes:
1. Students will be able to understand Artificial Neural Network concept with the help of Biological Neural Network.
2. Students will be able to implement algorithms to train ANN by using learning algorithms.
3. Students will be able to test fuzzy set operations and binary relations.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: Bachelor of Engineering
Branch: Electronics & Telecommunication
Subject: RF Communication Design
Code: 328747(28)

Total Theory Periods: 40
Class Tests: Two (Minimum)
ESE Duration: Three Hours
Total Tutorial Periods: 12
Assignments: Two (Minimum)
Maximum Marks: 80
Minimum Marks: 28

Course Objectives:
1. To understand concepts of Radio frequency design.
2. To gain knowledge of smith Chart fundamentals.
3. To understand concepts of RF filter design.
4. To have insight into active RF components.
5. To learn modeling using active RF components.

UNIT-I

UNIT-II

UNIT-III

UNIT-IV

UNIT-V

Text Books:
1. RF Circuit Design Theory And Application by Reinhold Ludwig 2nd Edition Pearson Ed.
2. RF Circuit Design by Christopher Bowick, Newnes.
3. Wireless Communication Electronics: Introduction to RF circuits, Springer India Ltd; (2014)

Reference Books:

Course Outcomes:
1. Students will be able to understand Importance of Radiofrequency Design.
2. Students will be able to understand concepts of Smith Chart.
3. Students will be able to understand Designing Concepts.
4. Concepts of active RF components is made clear.
5. Students will be able to understand Modeling concepts of active RF components is done and measurement of various parameters of semiconductor devices.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of program: Bachelor of Engineering
Branch: Electronics & Telecommunication
Subject: VLSI System Design
Semester: VII
Code: 328748(28)

<table>
<thead>
<tr>
<th>Total Theory Periods:</th>
<th>40</th>
<th>Total Tutorial Periods:</th>
<th>12</th>
</tr>
</thead>
<tbody>
<tr>
<td>Class Tests:</td>
<td>Two (Minimum)</td>
<td>Assignments:</td>
<td>Two (Minimum)</td>
</tr>
<tr>
<td>ESE Duration:</td>
<td>Three Hours</td>
<td>Maximum Marks:</td>
<td>80</td>
</tr>
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<td></td>
<td></td>
<td>Minimum Marks:</td>
<td>28</td>
</tr>
</tbody>
</table>

Course Objectives:
1. To understand concepts of Minimization and Transformation of Sequential Machines.
2. To gain knowledge for digital designing.
3. To gain knowledge about SM chart.
4. To understand Fault Modeling & Test Pattern Generation.
5. To be able to diagnose fault in sequential circuits.

UNIT-I Minimization and Transformation of Sequential Machines: The Finite State Model: Capabilities and Limitations of FSM, State Equivalence and Machine, Minimization-Simplification of Incompletely Specified Machines, Fundamental Mode Model, Flow Table, State Reduction, Minimal Closed Covers, Races, Cycles and Hazards.

UNIT-II Digital Design: Digital Design Using ROMs, PALs and PLAs, BCD Adder, 32-bit Adder, State Graphs for Control Circuits, Scoreboard and Controller, A Shift and Add Multiplier, Array Multiplier, Keypad Scanner, Binary Divider.

UNIT-III SM Charts: State Machine Charts, Derivation of SM Charts, Realization of SM Chart, Implementation of Binary Multiplier, Dice Game Controller.


Text Books:
3. Logic Design Theory by N. N. Biswas, PHI.

Reference Books:

Course Outcomes:
1. Students will be able to understand minimization and transformation of sequential machines
2. Students will be able to design different combinational circuits.
3. Students will be able to design and implementation using SM chart.
4. Students will be able to generate test pattern and able to diagnose fault in sequential circuits.
Chhattisgarh Swami Vivekanand Technical University, Bilai

Name of program: Bachelor of Engineering
Branch: Electronics & Telecommunication
Subject: Digital Image Processing

Semester: VII
Code: 328749(28)

Total Theory Periods: 40
Class Tests: Two (Minimum)
ESE Duration: Three Hours

Total Tutorial Periods: 12
Assignments: Two (Minimum)
Maximum Marks: 80
Minimum Marks: 28

Course Objectives:
1. To know the basic components of an image processing system.
2. To understand the basics of the human visual system as they relate to image processing including spatial frequency resolution and brightness adaptation.
3. To teach the students about various image enhancement techniques and transformation of images.
4. To have an illustrative idea about various edge detection techniques.
5. To give knowledge about the need of thresholding and types of thresholding techniques.
6. To have a brief idea about approaches to restoration and image compressions.

UNIT I

UNIT II

UNIT III
Image Segmentation: Some Basic Relationships between Pixels, Point, Line and Edge Detection, Gradient Operators, Canny Edge Detection, Pyramid Edge Detection, Edge Linking and Boundary Detection, Hough Transform, Chain Codes, Boundary Segments, Skeletons, Boundary Descriptors, Fourier Descriptors.

UNIT IV
Thresholding: The Role of Illumination, Global Thresholding, Adaptive Thresholding, Use of Boundary Characteristics for Histogram Improvement and Local Thresholding, Region based Segmentation, Region Growing, Region Splitting and Merging.

UNIT V

Text Books:
3. Fundamental of Digital Image Processing by A K Jain, PHI.

Reference Book:
3. Digital Image Processing by Madhuri A. Joshi, PHI.

Course Outcomes:
1. Students will understand how images are represented; Understand image types such as binary images, gray-scale images, color and multi-spectral images.
2. Emphasis will be to develop engineering skills and intuitive understanding of the tools used in Image Processing.
3. Students will be able to do various operations on images like Image enhancement, transformation, sharpening etc.
4. Students can analyze various edge detection techniques and their algorithms.
5. Students will be able to use various thresholding techniques and segmentations.
6. Students will be able to visualize approaches used in image restoration.