

Chhattisgarh Swami Vivekanand Technical University Bhilai (C.G.)

SCHEME OF EXAMINATION

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

M.Tech. in Electrical Devices & Power System Engineering

4th SEMESTER

s.no .	Board of Study	Subject Code	Subject	Periods Per Week			Scheme of exam			Total Marks	Credit L+(T+P)/ 2
				L	T	P	Theory/Practical				
							ESE	CT	TA		
4	Electrical and Electronics Engg.	570421 (25)	Dissertation + Seminar + Viva- Voce	6	-	34	300	-	200	500	23
TOTAL				6	-	34	300	-	200	500	23

Lecture T- Tutorial P- Practical ESE- End Semester Exam

CT- Class Test TA- Teachers Assessment