Chhattisgarh Swami Vivekanand Technical University, Bhilai
Scheme of Teaching & Examination
B.E. VI SEMESTER
APPLIED ELECTRONICS AND INSTRUMENTATION

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Board of Studies</th>
<th>Subject Code</th>
<th>Subject Name</th>
<th>Periods Per Week</th>
<th>Scheme of Exam. (Theory/ Practical)</th>
<th>Total Marks</th>
<th>Credit L+(T+P)/2</th>
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<tbody>
<tr>
<td>1</td>
<td>Electronics and Instrumentation</td>
<td>312651(27)</td>
<td>Process Dynamics &amp; Control</td>
<td>3 1 1</td>
<td>80 20 20</td>
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<td>312652(27)</td>
<td>Programmable Logic Controller</td>
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<td>Virtual Instrumentation</td>
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<td>Microcontroller &amp; Embedded Systems</td>
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<td>Refer Table - 1</td>
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<td>Professional Elective -1</td>
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L: Lecture  
ESE: End Semester Examination  
T: Tutorial  
P: Practical  
CT: Class Test  
TA: Teachers’ Assessment

Note: Industrial Training of eight weeks is mandatory for B.E. students. It is to be completed in two equal parts. The first part must have been completed in summer after IV semester The second part to be completed during summer after VI semester after which students have to submit a training report which will be evaluated by college teachers during B.E. VII semester

Table - 1 (Professional Electives – I)

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Board of Studies</th>
<th>Subject Code</th>
<th>Subject Name</th>
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<tbody>
<tr>
<td>1</td>
<td>Electronics and Instrumentation</td>
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<td>Data Structure using 'C++'</td>
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<td>Electronics and Instrumentation</td>
<td>312672(27)</td>
<td>Microelectronic Devices &amp; VLSI Technology</td>
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<td>3</td>
<td>Electronics and Instrumentation</td>
<td>312673(27)</td>
<td>Telecommunication Switching &amp; Computer Network</td>
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<td>4</td>
<td>Electronics and Instrumentation</td>
<td>312674(27)</td>
<td>Microwave Instrumentation</td>
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<td>Electronics and Instrumentation</td>
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<td>Semiconductor Components &amp; Materials</td>
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<td>6</td>
<td>Electronics and Instrumentation</td>
<td>312676(27)</td>
<td>Telematics</td>
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Note:
1. 1/4th of total strength of students to minimum of twenty students is required to offer an elective in the college in a particular academic session.
2. Choice of elective course once made for an examination cannot be changed in future examinations.

Name of the Programme: Bachelor of Engineering  ::::  Duration of the Programme: Four Years
Name of the Programme: Bachelor of Engineering

Duration of the Programme: Four Years

<table>
<thead>
<tr>
<th>Branch:</th>
<th>Electronics &amp; Instrumentation/Applied Electronics &amp; Instrumentation</th>
<th>Semester:</th>
<th>VI</th>
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<td>Subject:</td>
<td>Process Dynamics &amp; Control</td>
<td>Code:</td>
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<td>Total Theory Periods:</td>
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<td>No. of class Tests to be conducted:</td>
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<tr>
<td>ESE Duration:</td>
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<td>80</td>
</tr>
<tr>
<td>Minimum Marks in ESE:</td>
<td>28</td>
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</table>

Course Objectives:
- To develop and apply mathematical & engineering skills to identify, formulate, and solve industrial process problems.
- This subject seeks to close the gap between Instrumentation & Mechanical Engineering.
- This subject provides the knowledge of different types of controller & their application in plant such as oil & refinery.

Course Outcomes: The students will be able to:
- Know the importance of on-off, proportional, integral and derivative modes, composite control modes - PI, PD and PID control modes.
- Understand different tuning methods like continuous cycling method, Ziegler - Nichol's tuning.

UNIT I  

UNIT II  
Basic Control Actions: Characteristics of on-off, proportional, single -speed floating control, integral and derivative modes, composite control modes - PI, PD and PID control modes , Response of controllers for different types of test inputs, Typical control schemes for level, flow, pressure and temperature

UNIT III  
Control Scheme & Controller Tunings: Tuning of controllers by process reaction curve method, Ziegler - Nichol's tuning, Feed Forward control, Ratio control, cascade control, averaging control, multivariable control, split range control, Inferential control, Batch and continuous process.

UNIT IV  

UNIT V  
Applications: Distillation column, control of top and bottom product compositions, reflux ratio, control of chemical reactor, control of heat exchangers, steam boiler, drum level control and combustion, drying, Filtration, humidification /dehumidification.

Text Books:

Reference Books:
Chhattisgarh Swami Vivekanand Technical University, Bhilai

<table>
<thead>
<tr>
<th>Course</th>
<th>Code</th>
<th>Description</th>
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<tr>
<td>ESE Duration</td>
<td>312652(27)</td>
<td>Programmable Logic Controller</td>
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<td>Maximum Marks in ESE</td>
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<td>80</td>
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<tr>
<td>Minimum Marks in ESE</td>
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</table>

**Course Objectives:**
- Helps Students to know about basic PLC.
- To know about various applications of PLC.
- To gain knowledge about basic component of PLC.
- To gain knowledge about programming procedure of PLC.

**Course Outcomes:** The students will be able to:
- Understand basic structure of PLC, overview of PLC systems- Input/Output modules.
- Know the MCR functions and various applications of different jump functions and Analog PLC operation.
- Understand, Communication in DCS, DCS system integration with PLC and computers, Data loggers, Data acquisition systems (DAS), computer control hierarchy levels and Direct Digital control (DDC).

**UNIT I**
**Introduction of PLC:** Definition and history- block diagram, basic structure of PLC, overview of PLC systems- Input/Output modules, input/output analog devices, input/output digital devices, - Power supplies –ISO slots, PLC advantage and disadvantages

**UNIT II**
**Basics programming of PLC:** General PLC programming procedures, PLC scanning, PLC input instructions, operational procedures, digital logic gates, Boolean algebra plc programming, PLC registers.

**UNIT III**
**PLC Intermediate Functions:** PLC basic functions - timer functions- on delay timer, off delay timer, counter functions- up counter, down counter, Arithmetic functions - PLC Addition and subtraction, multiplication, division and square root function, PLC trigonometric and log functions, Number comparison functions.

**UNIT IV**
**PLC advanced Functions:** The skip function and applications, MCR functions and applications, jump functions- jump with non return, jump with return. Analog PLC operation, types of analog modules, PLC analog signal processing, BCD or multibit data processing, PLC-PID functions.

**UNIT V**
**Implementation of DCS:** Evolution of DCS advantages of digital computer control, Communication in DCS, DCS system integration with PLC and computers, Data loggers: Data acquisition systems (DAS): alarms, computer control hierarchy levels. Direct Digital control (DDC), Supervisory digital control (SCADA)

**Text Books:**

**Reference Books:**
3. Hughes, T: “Programmable logic controllers”, ISA Press.

**Name of the Programme: Bachelor of Engineering**  
**Duration of the Programme: Four Years**
Chhattisgarh Swami Vivekanand Technical University, Bilai

<table>
<thead>
<tr>
<th>Branch: Applied Electronics &amp; Instrumentation</th>
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<tr>
<td>Subject: Virtual Instrumentation</td>
<td>Code: 312653(12)</td>
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<td>Total Theory Periods: 40</td>
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<td>No. of class Tests to be conducted: 2 (Minimum)</td>
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<td>ESE Duration: Three Hours</td>
<td>Maximum Marks in ESE: 80</td>
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<td>Minimum Marks in ESE: 28</td>
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</table>

**Course Objectives:**
- To develop and apply mathematical & engineering skills to identify, formulate, and solve Virtual Simulation Programming.
- This subject seeks to close the gap between Engineering & Industrial field.
- This subject provides the knowledge of different types of Virtual Programming Technique used in Industries & Production Companies.

**Course Outcomes:** The students will be able to:
- Understand Basics of Lab VIEW Programming and their application in different Industrial Instruments.
- Know the Performance of Virtual Programming.
- Understand an identification & their measurement system used in Industrial Sensors.
- Identify the problems encountered in measurement and control of Programming.

**UNIT I** Review of Virtual Instrumentation: Historical perspective, advantages, block diagram and architecture of a virtual instrument, data-flow techniques, graphical programming in data flow, comparison with Conventional programming.

**UNIT II** VI Programming Techniques: VIS and sub-VIS, loops and charts, arrays, clusters and graphs, case and sequence structures.

**UNIT III** Data Acquisition Basics: formula nodes, local and global variables, string and file I/O, counters & timers, software and hardware installation.

**UNIT IV** Use of Analysis Tools: Fourier transforms, power spectrum, correlation methods, windowing & filtering. VI applications in various fields.

**UNIT V** Common Instrument Interfaces: Current loop, RS 232C/ RS485, GPIB, System buses, interface buses: USB, PCMCIA, VXI, SCXI, PXI, etc., networking basics for office & Industrial applications, VISA and IVI, image acquisition and processing. Motion control.

**Text Books:**
2. Lisa K. wells & Jeffrey Travis, Labview for everyone, Prentice Hall.

**Reference Books:**
1. Sokoloff, Basic concepts of Labview 4, Prentice Hall.

Name of the Programme: Bachelor of Engineering   ::::   Duration of the Programme: Four Years
Chhattisgarh Swami Vivekanand Technical University, Bhilai

<table>
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<th>Branch:</th>
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<td>Subject:</td>
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**Course Objective**
- To provide knowledge about the Discrete Fourier Transform and the FFT Algorithms.
- To provide knowledge about the Applications of IIR & FIR filters.
- To provide knowledge about the Realization of digital filters.
- To make familiar the students about the Applications of DSP in various fields.

**Course Outcomes:** The students will be able to:
- Understand Basics knowledge and their application in different Digital Filters.
- Know the Applications of Digital Signal Processors.
- Understand of various Finite Impulse Response Filters.
- Identify the Discrete Fourier Transform and Fast Fourier Transform.

**UNIT I**
**DFT and FFT:** Introduction, Discrete Fourier Transform (DFT), Properties of DFT, Computation of DFT & IDFT, Relationship of the DFT to other transforms. Fast Fourier Transform (FFT), Radix-2 Decimation In Time (DIT) and Decimation In Frequency (DIF) FFT Algorithms, Inverse FFT Computation, Comparison between DIT and DIF Algorithms.

**UNIT II**

**UNIT III**
**Finite Impulse Response Filters:** Introduction, Magnitude and Phase Response of Digital Filters, Linear Phase FIR Filters, Frequency Response of Linear Phase FIR Filters, Design of FIR Filter using Window Techniques, Design of FIR Filter using Frequency Sampling Method, Comparison between IIR & FIR Filters.

**UNIT IV**
**Realization of Digital Filters:** Realization of IIR Filters- Direct Form-I, Direct Form-II, Cascade Form and Parallel Form Realizations. Lattice Structure and Lattice-Ladder Structure. Realization of FIR Filters- Direct Form, Cascade Form and Linear Phase Realizations. Lattice Structure and Polyphase Realization.

**UNIT V**

**Name of Text Books:**
1. “Digital Signal Processing” by Salivahanan, Vallavaraj, Gnanapriya, TMH
2. “Digital Signal Processing” by P. Ramesh Babu, SCITECH

**Name of Reference Books:**
3. “Digital Signal Processing” by Proakis, Manolakis& Sharma, Pearson Education

Name of the Programme: Bachelor of Engineering
Duration of the Programme: Four Years
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Branch: Electronics & Telecommunication
Semester: VI
Electronics & Instrumentation
Engineering/Electronics & Instrumentation/Applied
Subject: Microcontroller & Embedded System
Code: 312655(28)
Duration of the Programme: Four Years
Total Theory Periods: 40
Total Tutorial Periods: 12
No. of class Tests to be conducted: 2 (Minimum)
No. of assignments to be submitted: 2 (Minimum)
ESE Duration: Three Hours
Maximum Marks in ESE: 80
Minimum Marks in ESE: 28

Course objectives:
- To make students familiar with the basic blocks of microcontroller device and Embedded system in general.
- To provide comprehensive knowledge of the architecture, features and interfacing with 8051 microcontroller.
- To use assembly and high level languages to interface the microcontrollers to various applications.

Course Outcome:
1. To understand Microcontroller 8051 its architecture and its instruction set.
2. Gain knowledge about Counter/timer and interrupts in 8051 Microcontroller and Programming concepts.
3. Students will be able to do serial communication programming and gain knowledge of serial communication.
4. Students will be able to understand interfacing Microcontroller 8051 with devices.

UNIT I Introduction to Microcontroller: A brief History of Microcontrollers, Harvard Vs Von-Neumann Architecture; RISC Vs CISC, Classification of MCS-51 family based on their features (8051,8052,8031,8751,AT89C51), Pin configuration of 8051.

8051 Processor Architecture and Instruction Set: Registers of 8051, Inbuilt RAM, Register banks, stack, on-chip and external program code memory ROM, power reset and clocking circuits, I/O port structure, Addressing modes, Instruction set and programming.

UNIT II Counter/Timer and Interrupts of 8051: Introduction, Registers of timer/counter, Different modes of timer/counter, Timer/counter programming, Interrupt Vs Polling, Types of interrupts and vector addresses, register used for interrupts initialization, programming of external interrupts, Timer interrupts.


UNIT IV Interfacing with 8051: Interfacing and programming of: ADC (0804,0808/0809,0848) & DAC (0808), stepper motor, 4x4 keyboard matrix, Relays, LED and Seven segment display, LCD, Interfacing (only) of different types of Memory, Address decoding techniques


Names of Text Books:
1. The 8051 Microcontroller and Embedded Systems using Assembly and C, Mazidi, Mazidi & McKinlay, 2nd Ed.,PHL.(Unit-I,II,III)
2. Embedded system, Frank Vahid.(Unit-IV)

Names of Reference Books:
2. 8 bit Microcontrollers & Embedded Systems Manual.
3. Programming and Customizing the 8051 Microcontroller, Predko; TMH
List of Experiments: (At least ten experiments are to be performed by each student)

1. To study the input and output module.
2. To study run, uploading and downloading of plc programming.
3. To Study the interfacing of PLC with optical sensors.
4. To Study the interfacing of PLC with proximity sensors.
5. To design and simulate of simple logic gate programming.
6. To design and simulate of universal logic gate programming
7. Study of timer programming
8. Study of counter programming
9. To design and simulate of relay control by using PLC.
10. To Study the Speed Control of DC Motor by using PLC.
11. To Study the different types of Switches Control by using PLC.
12. Study of Analog Module for PLC
13. To design and simulate of Conveyor Control by PLC
14. To design and simulate of Water Level Control by PLC
15. To design and simulate of Elevator Control using PLC
16. To design and simulate of Traffic Light Control by PLC
17. To design and simulate of Temperature Control using PLC
18. To design and simulate of bottling plan.

List of Equipments/Machine Required:
Plc modules, power supply, different sensors and plc software.

List of Experiments: (At least Ten experiments are to be performed by each student)

1. Write a microcontroller 8051 program to transfer the bytes into RAM locations starting at 50H, assuming that ROM space starting at 240H contains CHHATTISGARH by using – a) Counter, b) null character for end of string.

2. Write a microcontroller 8051 program to get hex data on the range of 00-FFh from port 0 and convert it into decimal. Save the digits in R7, R6 and R5, where the least significant digit is in R7.

3. Write a microcontroller 8051 program to add two 16 Bit unsigned numbers. Operands are two RAM variables. Results to be in R1-R0 pair.

4. Write a microcontroller 8051 program to subtract an unsigned 16 Bit number from another. Operands are two RAM variables. Results to be in R1-R0 pair.

5. Write a microcontroller 8051 program to add two unsigned 32-bit numbers. Operands are two RAM variables. Results to be in R1-R0 pair.

6. Write a microcontroller 8051 program to add two 16 Bit signed numbers.

7. Write a microcontroller 8051 program to convert a binary number to equivalent BCD

8. Write a microcontroller 8051 program to convert a packed BCD number to two ASCII numbers and place them in R5 and R6.

9. Write a microcontroller 8051 program to calculate the square root of an 8-bit number using iterative method.

10. Write a microcontroller 8051 program that generates 2 kHz square wave on pin P1.0, 2.5 kHz on pin P1.2 and 25 Hz on pin P1.3.

11. Write a microcontroller 8051 program for counter 1 in mode 2 to count the pulses and display the state of the TL1 count on P2. Assume that the clock pulses are fed to pin T1.

12. Write a microcontroller 8051 program to transfer letter “N” serially at 9600 baud, continuously. Assume crystal frequency to be 11.0592 MHz.

13. Write a microcontroller 8051 program to transfer word “CSVTU” serially at 4800 baud and one stop bit, continuously. Assume crystal frequency to be 11.0592 MHz.

14. Write a microcontroller 8051 program to receive bytes of data serially, and put them in P1. Set the baud rate at 2400 baud, 8-bit data, and 1 stop bit. Assume crystal frequency to be 11.0592 MHz.

List of Equipments/Machine Required:
Microcontroller kit, Interfacing kit, Keyboard, Monitor, SMPS for Micro controller.
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Branch: Applied Electronics & Instrumentation
Subject: Digital Signal Processing Laboratory

Semester: VI
Code: 312663 (12)

Total Lab Periods: 36
Batch Size: 30

Maximum Marks: 40
Minimum Marks: 20

List of Experiments: (At least ten experiments are to be performed by each student)

List of Experiments to be performed:

1. To generate Analog Signals.
2. To sample a sinusoidal signal at Nyquist rate
3. To convolve two given signals
4. To correlate two given signals
5. To design LPF using recursive structures
6. To design HPF using recursive structure
7. To design BPF using recursive structure
8. To design BSF using recursive structure
9. To design LPF using non-recursive structures
10. To design HPF using non-recursive structure
11. To design BPF using non-recursive structure
12. To design BSF using non-recursive structure
13. To generate Discrete sequences.
14. To calculate DFT and IDFT of a given sequence.
15. To calculate FFT of a given sequence.
16. To design IIR Filter using Impulse Invariant Method.
17. To design IIR Filter using Bilinear Transformation.
18. To design FIR Filter using Frequency Sampling Method.

(Institutes may append more Programmes/Experiments based on the infrastructure available)

List of Equipments/Machine Required:

MATLAB with Tool boxes, DSP Processor Kit.

Recommended Book: Digital Signal Processing by Salivahanan, Vallavaraj, Gnanapriya, Tata McGraw Hill publisher
### Chhattisgarh Swami Vivekanand Technical University, Bhilai

**Branch:** Applied Electronics & Instrumentation  
**Semester:** VI  
**Subject:** Electronic Simulation Laboratory  
**Code:** 312664 (12)

<table>
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<th>Total Lab Periods</th>
<th>Maximum Marks</th>
<th>Batch Size</th>
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</thead>
<tbody>
<tr>
<td>36</td>
<td>40</td>
<td>30</td>
<td>20</td>
</tr>
</tbody>
</table>

### List of Experiments: (At least ten experiments are to be performed by each student)

1. To Design, implement and Simulate Fixed bias and self bias transistorized circuit for determining the bandwidth
2. To Design, implement and Simulate Fixed bias and self bias for studying the low frequency and high frequency effect.
3. To Design, implement and Simulate Miller integrator for determining the nonlinearities.
4. To Design, implement and simulate current Sweep generator for determining the nonlinearities.
5. To Design, implement and Simulate Inverting and non inverting amplifier for determining the bandwidth
6. To Design, implement and Simulate Integrator & differentiator for studying output responses for different inputs.
7. To Design, implement and Simulate zero crossing detector & comparator for studying output responses for different inputs.
8. To Design, implement and Simulate Series Voltage regulator.
9. To Design, implement and Simulate 1st & 2nd order LPF for determining the bandwidth and studying output responses for different inputs.
10. To Design, implement and Simulate 1st & 2nd order HPF for determining the bandwidth
11. To Design, implement and Simulate Half wave & Full wave rectifier way op-Amp for determining the bandwidth.
12. To Design, implement and Simulate Series and Shunt Clipper for studying output responses
13. To Design, implement and Simulate Clamping circuit for studying output responses for different inputs
14. To Design, implement and Simulate Clamping Circuit with op-Amp for studying output responses for different inputs
15. To Design, implement and Simulate Instrumentation Amplifier using three op-Amp for determining the bandwidth
16. To Design, implement and Simulate Monostable&Astable using 555 timer
17. To Design, implement and Simulate R -2R ladder type Digital to analog converter
18. To Design, implement and Simulate Flash type Analog to digital

### List of Equipments/Machine Required:
Lab VIEW, Desktop PCs, Simulation Software for Analog Circuits like MULTISIM, PSPICE etc.

### Recommended Books:
1. Experiments and SPICE Simulations in Analog Electronics Laboratory, Maheswari&Anand, PHI
2. Manuals of MUSLTISIM
3. Manuals of PSPICE

**Name of the Programme:** Bachelor of Engineering  
**Duration of the Programme:** Four Years
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Name of Program: **Bachelor of Engineering**
Branch: **Common to All Branches**
Subject: **Managerial Skills**
No. of Lectures: **2/Week**
Total Marks in ESE: **NIL**

Duration of the Programme: **Four Years**

Minimum number of Class Tests to be conducted: **Two**

**Objective:**
The course is introduced to develop managerial skills tremendously and enrich the abilities to enable one to meet the challenges associated with different job levels. Managerial skills are essential for overall professional development of an individual apart from gaining technical knowledge in the subject.

**Course Objectives**
Upon completion of this course, the student shall be able
- To define and explain the concept of managerial, written and oral communication skill;
- To understand the leadership skill;
- To develop self-appraisal and understand distinction between leader and manager;
- To develop positive attitude and thinking; and
- To understand managerial functions and develop creativity.

**UNIT I Managerial Communication Skills:** Importance of Business Writing: writing business letters, memorandum, minutes, and reports- informal and formal, legal aspects of business communication, oral communication- presentation, conversation skills, negotiations, and listening skills, how to structure speech and presentation, body language.

**UNIT II Managerial skills - Leadership:** Characteristics of leader, how to develop leadership; ethics and values of leadership, leaders who make difference, conduct of meetings, small group communications and Brain storming, Decision making, How to make right decision, Conflicts and cooperation, Dissatisfaction: Making them productive.

**UNIT III Proactive Manager:** How to become the real you: The journey of self-discovery, the path of self-discovery, Assertiveness: A skill to develop, Hero or developer, Difference between manager and leader, Managerial skill check list, team development, How to teach and train, time management, Stress management, Self-assessment.

**UNIT IV Attitudinal Change:** Concept of attitude through example, benefits of right attitude, how to develop habit of positive thinking, what is fear? How to win it? How to win over failure? How to overcome criticism? How to become real you? How to Motivate? How to build up self confidence?

**UNIT V Creativity:** Creativity as a managerial skill, Trying to get a grip on creativity. Overview of Management Concepts: Function of Management: Planning, organizing, staffing, controlling.

**Course Outcome**
- The students will be able to develop formal and informal, negotiation, written and oral communication skill;
- The students will be able to develop manage groups, resolve conflicts and leadership skill and decision making qualities;
- The students will be able to develop self-appraisal, teaching, training and managing stress and time;
- The students will be able develop positive thinking, motivating team members and winning race; and
- The students will be able to develop creativity and fundamental management functions.

**Text Books:**

**Reference Books:**
1. How to develop a pleasing personality by Atul John Rego, Better yourself  books, Mumbai,2006
2. The powerful Personality by Dr. Ujjawal Patni & Dr. Pratap Deshmukh, Fusion Books, 2006
3. How to Success by Brian Adams, Better Yourself books, Mumbai, 1969

Name of the Programme: Bachelor of Engineering :::: Duration of the Programme: Four Years
Bachelor of Engineering

Duration of the Programme: Four Years

Name of the Programme: Bachelor of Engineering

Chhattisgarh Swami Vivekanand Technical University, Bhilai

Branch: Electronics & Telecommunication Engineering /Electronics & Instrumentation /Applied Electronics & Instrumentation

Subject: Data Structure using ‘C++’ (Professional Elective – I)

Duration: Three Hours

Maximum Marks in ESE: 80
Minimum Marks in ESE: 28

Course Objective:
- To provide knowledge about C++
- To make students aware about features of object oriented property.
- To provide knowledge about algorithms and their implementation.
- To provide knowledge about Dictionaries & Priority Queues.

Course Outcomes: The students will be able to:
- Understand Objects, Class Members, Access Control, Class Scope, Constructors and destructors, parameter passing methods, Inline functions and friend functions.
- Know the Operator Overloading, Inheritance basics, base and derived classes, inheritance types, base class access control.
- Understand Realizing a Priority Queue using Heaps and External Sorting- Model for external sorting.

UNIT I
C++ Class Overview- Class Definition, Objects, Class Members, Access Control, Class Scope, Constructors and destructors, parameter passing methods, Inline functions, static class members, this pointer, friend functions, dynamic memory allocation and deallocation (new and delete), features of object oriented property.

UNIT II
Function Overloading, Operator Overloading, Generic Programming- Function, Inheritance basics, base and derived classes, inheritance types, base class access control, runtime polymorphism using virtual functions, abstract classes, exception handling.

UNIT III
Algorithms, performance analysis- time complexity and space complexity. Review of basic data structures- The list ADT, Stack ADT, Queue ADT.

UNIT IV
Dictionaries, linear list representation, skip list representation, operations insertion, deletion and searching, hash table representation, hash functions, collision resolution-separate chaining, open addressing-linear probing, quadratic probing, double hashing, rehashing, extendible hashing, comparison of hashing and skip lists.

UNIT V
Priority Queues: Definition, ADT, Realizing a Priority Queue using Heaps, Definition, insertion, Deletion, External Sorting- Model for external sorting, Multiway merge, Polyphase merge.

Text Books:

Reference Books:
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Branch: Electronics & Telecommunication Engineering
Electronics & Instrumentation /Applied Electronics & Instrumentation
Subject: Microelectronic Devices & VLSI Technology
(Professional Elective – I)

<table>
<thead>
<tr>
<th>Total Theory Periods:</th>
<th>40</th>
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<tbody>
<tr>
<td>No. of class Tests to be conducted:</td>
<td>2 (Minimum)</td>
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<tr>
<td>ESE Duration:</td>
<td>Three Hours</td>
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<tr>
<td>Total Tutorial Periods:</td>
<td>NIL</td>
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<tr>
<td>No. of assignments to be submitted:</td>
<td>2 (Minimum)</td>
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<td>Periods:</td>
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<td>Marks:</td>
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<td>Maximum Marks in ESE:</td>
<td>80</td>
</tr>
<tr>
<td>Minimum Marks in ESE:</td>
<td>28</td>
</tr>
</tbody>
</table>

**Course Objective:**
- To develop the understanding of n MOS / p MOS Transistor, MOS Device Design Equation, CMOS Inverter.
- To understand the static CMOS designing of combinational logic gates.
- To develop an understanding of designing sequential logic circuits using FPGA & CPLD architecture.
- To understand the combinational Logic Design.

**Course Outcomes:** The students will be able to:
- Understand n MOS / p MOS Transistor, Threshold Voltage Equation, Body Effect, MOS Device Design Equation, Sub Threshold Region, Channel Length Modulation.
- Know the Static CMOS Design, NAND Gate, NOR Gate ,Ratiod Logic Pass transistor Logic, Transmission Gate.
- Understand Static Latch and Register, S-R Latch Circuit, D-Latch and Triggered Flip Flop, FPGA and CPLD Architecture.

**UNIT I** MOS Transistor Theory: n MOS / p MOS Transistor, Threshold Voltage Equation, Body Effect, MOS Device Design Equation, Sub Threshold Region, Channel Length Modulation. CMOS Inverter.

**UNIT II** Designing Combinational Logic Gates in CMOS-Static CMOS Design, NAND Gate, NOR Gate, Ratiod Logic Pass transistor Logic, Transmission Gate.

**UNIT III** Designing Sequential Logic Circuit : Introduction , Static Latch and Register, S-R Latch Circuit, D-Latch and Triggered Flip Flop, FPGA and CPLD Architecture, VLSI Design style with FPGA and CPLD.

**UNIT IV** Combinational Logic Design: Introduction to VHDL; Operators, Data Types, Libraries; Entity, Architecture; Data flow, Structural and Behavioral Programming, Generic, Signal, Generate, Process, Loops, Case, Variable, Procedure, Component and Configuration.

**UNIT V** Sequential Logic Design: Sequential Design by VHDL: Flip-Flop and Shift Registers; FSM: Moore and Mealy Machine, Counter, Sequence Detector, Basic Concepts of Operator Overloading, Blocks, Delays.

**Textbook:**

**References:**
2. VHDL Primer by J. Bhaskar, PHI
5. VHDL Programming by Perry, TMH Pub
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Branch: Electronics & Telecommunication Engineering /Electronics & Instrumentation /Applied Electronics & Instrumentation
Semester: VI

Subject: Telecommunication Switching & Computer Network (Professional Elective – I)
Code: 312673(27)

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<th>No. of class Tests to be conducted:</th>
<th>Total Theory Periods:</th>
<th>Total Tutorial Periods:</th>
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<tbody>
<tr>
<td>2 (Minimum)</td>
<td>40</td>
<td>NIL</td>
<td>2 (Minimum)</td>
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</table>

ESE Duration: Three Hours
Maximum Marks in ESE: 80
Minimum Marks in ESE: 28

Course Objective:
- Acquire fundamental knowledge of data communication, network criteria, transmission mode, switching system, multiplexing in computer networks.
- Acquire fundamental knowledge about services provided by data link layer and network layer and its protocol.
- Acquire fundamental knowledge about LAN and its types.
- Understand the concepts of OSI model, the major functions of each of the layers of the OSI and the Internet protocol and protocol architecture.

Course Outcomes:
- Knowledge about the data transmission, standard & protocol and transmission mode.
- Knowledge about the OSI model & its various layer and TCP/IP protocol suite
- Knowledge about LAN and its architectures.
- Knowledge about telecommunication switching system.


Text Books:
2. Vishwanathan Thiagarajan, “Telecommunication Switching Systems And Networks”, PHI.

Reference Books

Name of the Programme: Bachelor of Engineering :::: Duration of the Programme: Four Years
Course Objectives:
- Ability to know the basics of microwave in industries like Process industries and automation, Communication, Aerospace, Naval, Space and Military Services.
- Familiarity with the applications and control of microwave instruments.
- Capability to enter the microwave based field for higher studies and research and development.

Course Outcomes: The students will be able to:
- Understand Region and Band Designations, Advantages and Applications of Microwave.
- Know the Rectangular Cavity Resonator, Circular Cavity Resonator, Application of Cavity Resonators, Field Expressions for TM_{nmp} and TE_{nmp} modes in a Rectangular Cavity Resonator.

UNIT I Introduction of Microwave: History, Region and Band Designations, Advantages and Applications of Microwave.

UNIT II Cavity Resonators: Rectangular Cavity Resonator, Circular Cavity Resonator, Application of Cavity Resonators, Field Expressions for TM_{nmp} and TE_{nmp} modes in a Rectangular Cavity Resonator, Field Expression for TE_{a} TM_{nmp} modes in a Circular Cavity Resonator
Microwave Tubes: Klystrons, Travelling Wave Tube (TWT), Magnetrons. (Only theory and application)


Name of Text Books:
1. “Microwave and Radar Engineering” by M. Kulkarni, Umesh Publications
2. “Radar Systems and Radio Aids to Navigation” by Dr. A. K. Sen and Dr. A. B. Bhattacharya, Khanna Publishers

Name of Reference Books:

Name of the Programme: Bachelor of Engineering :::: Duration of the Programme: Four Years
Chhattisgarh Swami Vivekanand Technical University, Bhilai

Branch: Electronics & Telecommunication Engineering /Electronics & Instrumentation /Applied Electronics & Instrumentation
Subject: Semiconductor Components & Materials (Professional Elective – I)

Duration of the Programme: Four Years

Course Objective:
- To gain knowledge about properties of insulators in static field.
- To know about the basic properties of dielectric materials.
- To gain knowledge about magnetism.
- Familiarize Students with conductivity of metals.
- To gain knowledge about properties of insulators in alternating field.

Course Outcomes: The students will be able to:
- Understand Static dielectric constant, Polarization and dielectric constant, Atomic interpretation of the dielectric constant of monatomic gases.
- Understand Magnetic dipole moment of a current loop, Magnetization from a macroscopic viewpoint, orbital magnetic dipole moment and Angular momentum of two simple atomic models, Lenz's law and induced dipole moments.
- Know the diamagnetism, origin of permanent magnetic dipoles in matter, paramagnetic spin systems, some properties of ferromagnetic materials, spontaneous magnetization and the Curie weiss law.

UNIT I Dielectric Properties of Insulators in Static Fields: Static dielectric constant, Polarization and dielectric constant, Atomic interpretation of the dielectric constant of monatomic gases, Qualitative remarks on the dielectric constant of polyatomic molecules, Internal field in solids and liquids, Static dielectric constant of solids, Some properties of ferroelectrics materials, Spontaneous polarization, Piezoelectricity.

UNIT II Behavior of Dielectric in Alternating Fields: Frequency dependence of the electronic polarizability, Ionic polarization as a function of frequency, complex dielectric constant of non-dipolar solids, dipolar relaxation, dielectric losses.

UNIT III Magnetic Properties of Materials: Summary of concepts pertaining to magnetic fields, Magnetic dipole moment of a current loop, Magnetization from a macroscopic viewpoint, orbital magnetic dipole moment and Angular momentum of two simple atomic models, Lenz's law and induced dipole moments.


UNIT V Conductivity of Metals & Super Conductivity: Ohm's law and the relaxation time of electrons, Relaxation time, collision time, and mean free path, Electron scattering and the resistivity of metals, Heat developed in a current carrying conductor, Thermal conductivity of metals, Superconductivity, Type I & II super conductor, Schottkey diode.

Name of the Text Books:
Name of Reference Books:

Name of the Programme: Bachelor of Engineering :::: Duration of the Programme: Four Years
Branch: Electronics & Telecommunication Engineering /Electronics & Instrumentation /Applied Electronics & Instrumentation
Semester: VI

Subject: Telematics (Professional Elective – I)
Code: 312676(27)

Total Theory Periods: 40
No. of class Tests to be conducted: 2 (Minimum)

ESE Duration: Three Hours
Maximum Marks in ESE: 80
Minimum Marks in ESE: 28

Course Objective:
- To provide knowledge about basic operation of Telephone Switching and Traffic Engineering.
- To gain knowledge about various switching elements and multistage switching systems.
- To gain knowledge about cellular & mobile system and their operation.
- To gain knowledge about the IP telephony, its structure and various features.

Course Outcomes: The students will be able to:
- Understand simple telephone communication, basics of switching system, dialing mechanism, digital switching, stored program control, centralized SPC, distributed SPC.
- Know the features of GSM, services provided by GSM, radio link aspect, GSM architecture, GSM channel structure, call flow sequences, code division multiple access.
- Understand Voice over internet protocol (VoIP), VoIP signaling protocol, principle of quality of service, quality of service requirements.

UNIT I Telephone Switching and Traffic Engineering: Evolution of telecommunication, simple telephone communication, basics of switching system, dialing mechanism, digital switching, stored program control, centralized SPC, distributed SPC, software architecture of SPC, enhanced services in SPC, traffic engineering.

UNIT II Switching Networks: Single stage network, two stage networks, three stage network, four stage network, time division switching, time division space switching, time division time switching, time multiplexed space switching, time multiplexed time switching, combination switching, three stage combination switching.

UNIT III Mobile Cellular Telephony: Limitation of conventional mobile telephone system, communication frequency band, introduction of mobile cellular telephony, the cellular concept, cellular mobile telephone system, cellular system types, mobile environment, frequency reuse, co-channel interference, adjacent co-channel interference, handoff mechanism, frequency management, channel assignment, dropped call, cell splitting, mobile radio propagation.

UNIT IV Digital Cellular System: Global system for mobile communication, features of GSM, services provided by GSM, radio link aspect, GSM architecture, GSM channel structure, call flow sequences, code division multiple access.

UNIT V IP Telephony: Voice over internet protocol (VoIP), VoIP signaling protocol, principle of quality of service, quality of service requirements, integrated services, RSVP (Resources reservation protocol), differentiated services.

Text Book:

Reference Books:

Name of the Programme: Bachelor of Engineering :::: Duration of the Programme: Four Years