

## *Chhattisgarh Swami Vivekanand Technical University, Bhilai*

*M.E. Electronics & Telecommunication (Specialization in VLSI Design)*

### FOURTH SEMESTER

Sr. No.	Board of Study	Subject Code	Subject	Periods Per Week			Scheme of Examination			Total Marks	Credit L+(T+P)/2
							Theory / Practical				
				L	T	P	ESE	CT	TA		
1	Electronics & Telecom	560421 (28)	Project + Seminar on Project	6	-	34	300	-	200	500	23
<b>TOTAL</b>				<b>6</b>	<b>-</b>	<b>34</b>	<b>300</b>	<b>-</b>	<b>200</b>	<b>500</b>	<b>23</b>

L - Lecture  
P - Practical  
CT - Class Test

T - Tutorial  
ESC – End Semester Exam  
TA – Teachers Assessment

### Scheme of Marks Allotment

Semester	Total Marks	Grand Total
I	1000	<b>3000</b>
II	1000	
III	500	
IV	500	